

10

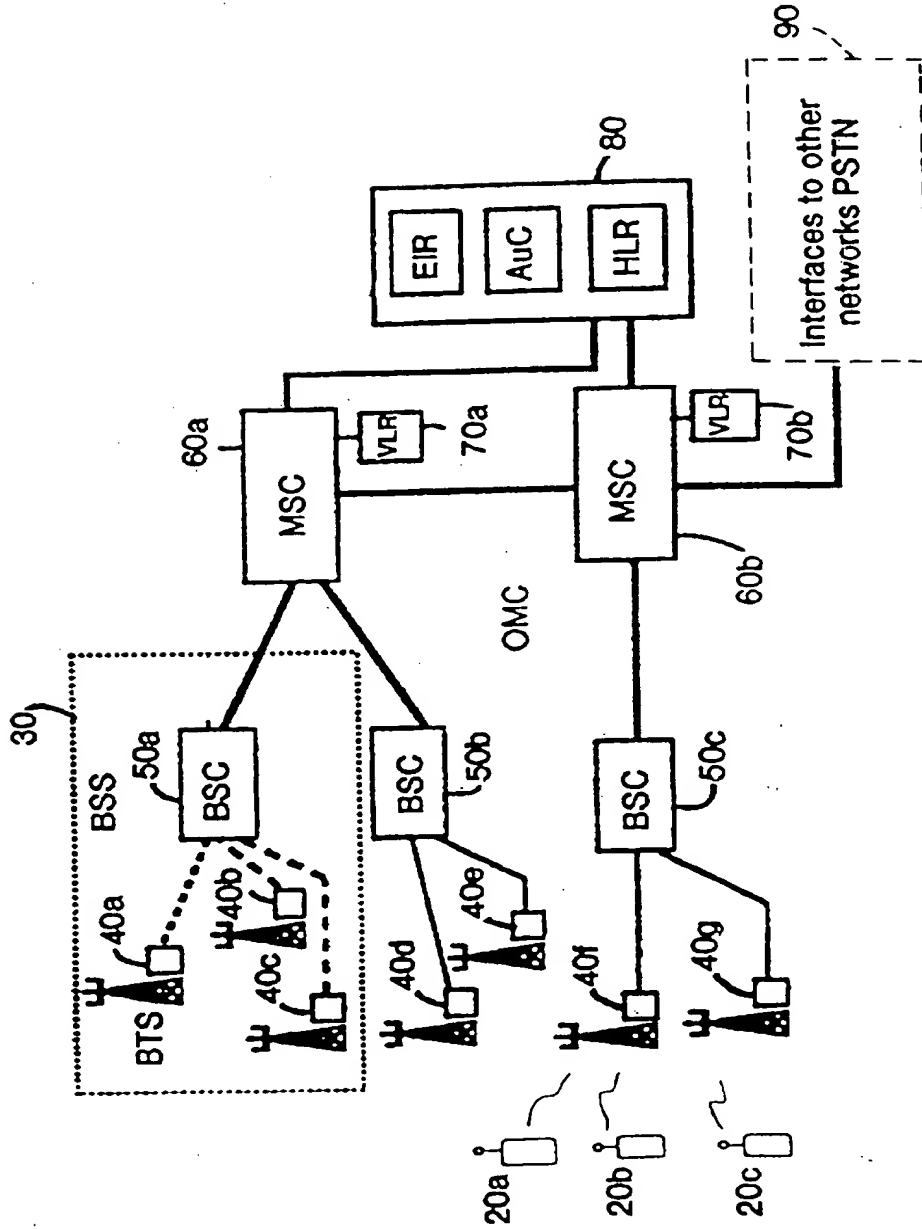
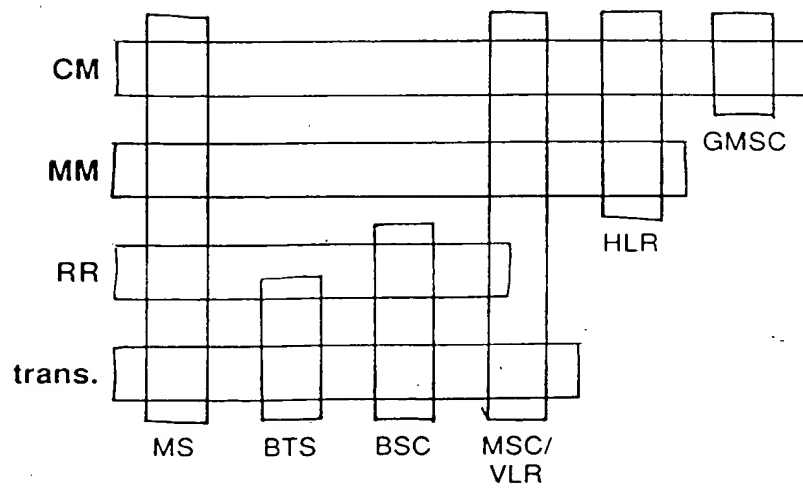
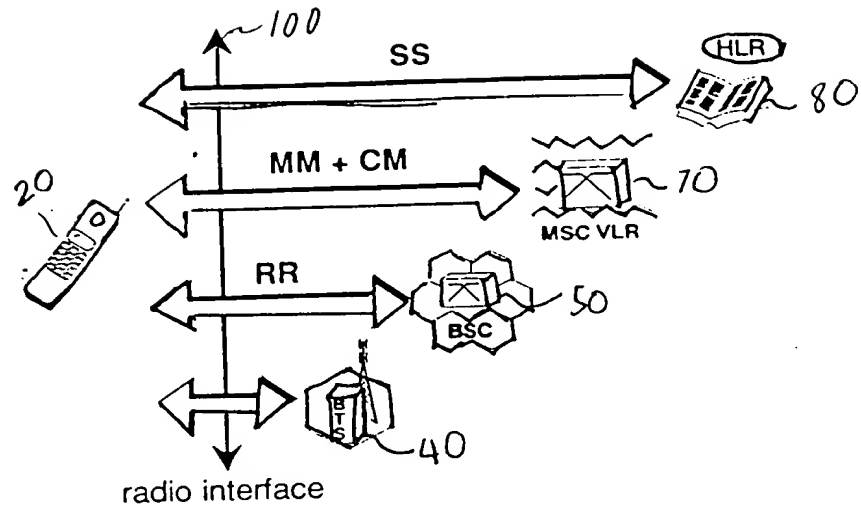


FIG. 1
(prior art)



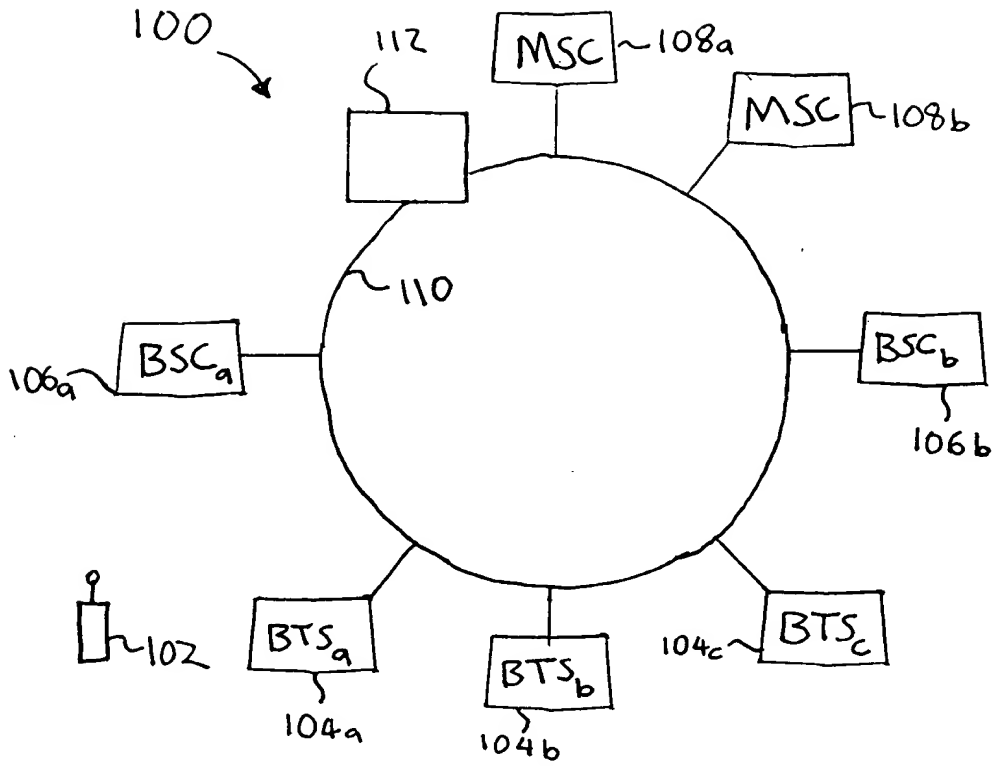


FIG. 4A

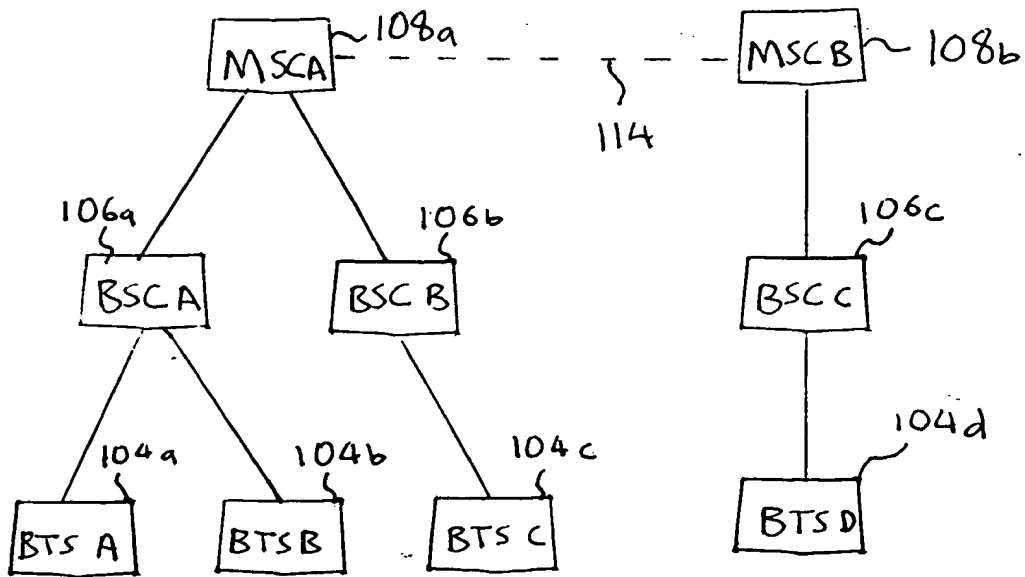


FIG. 4B

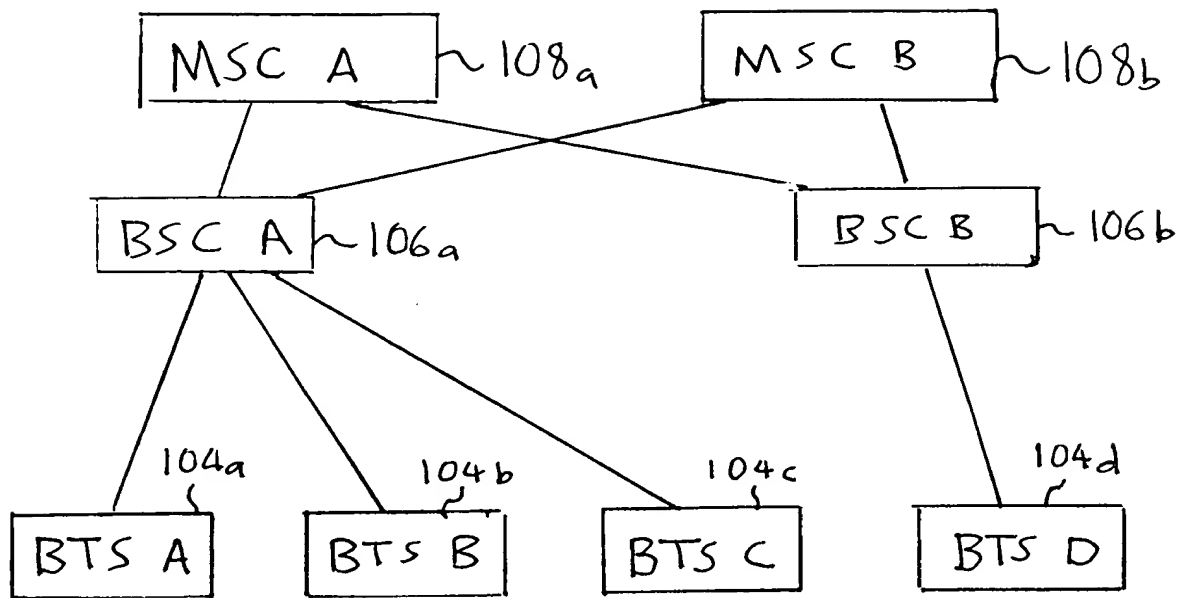


FIG. 4C

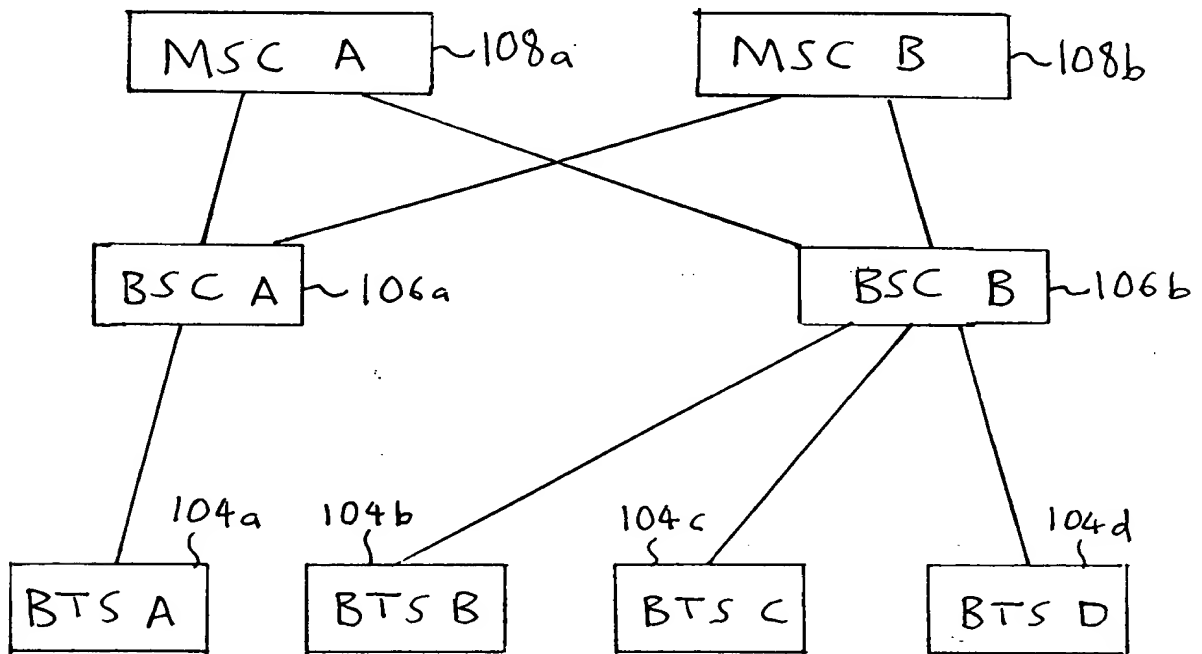


FIG. 4D

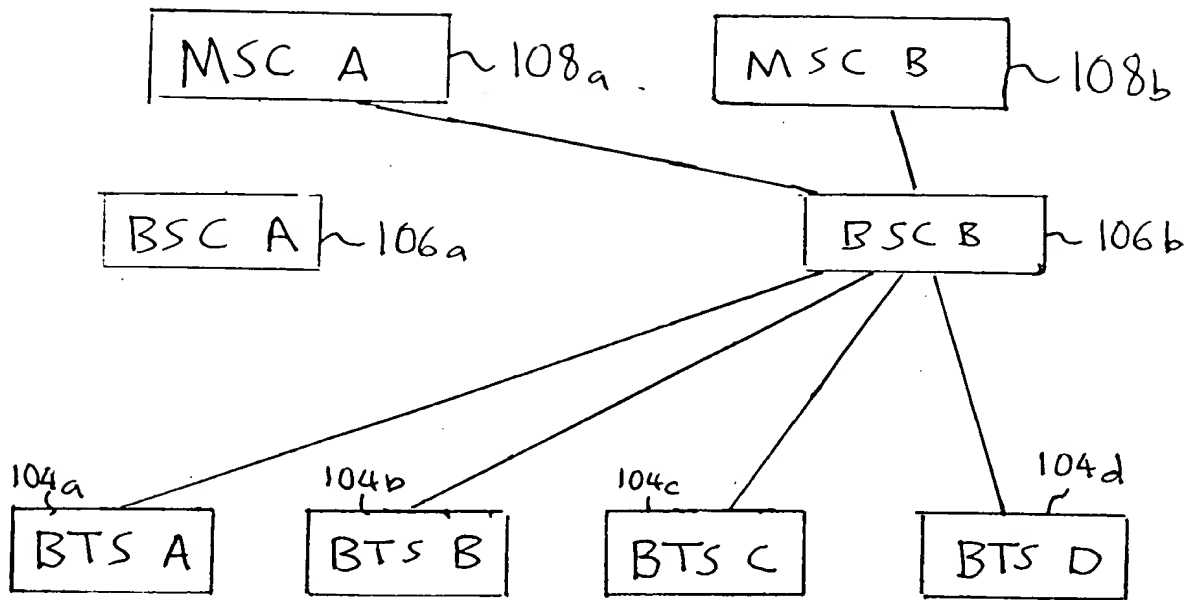


FIG. 4E

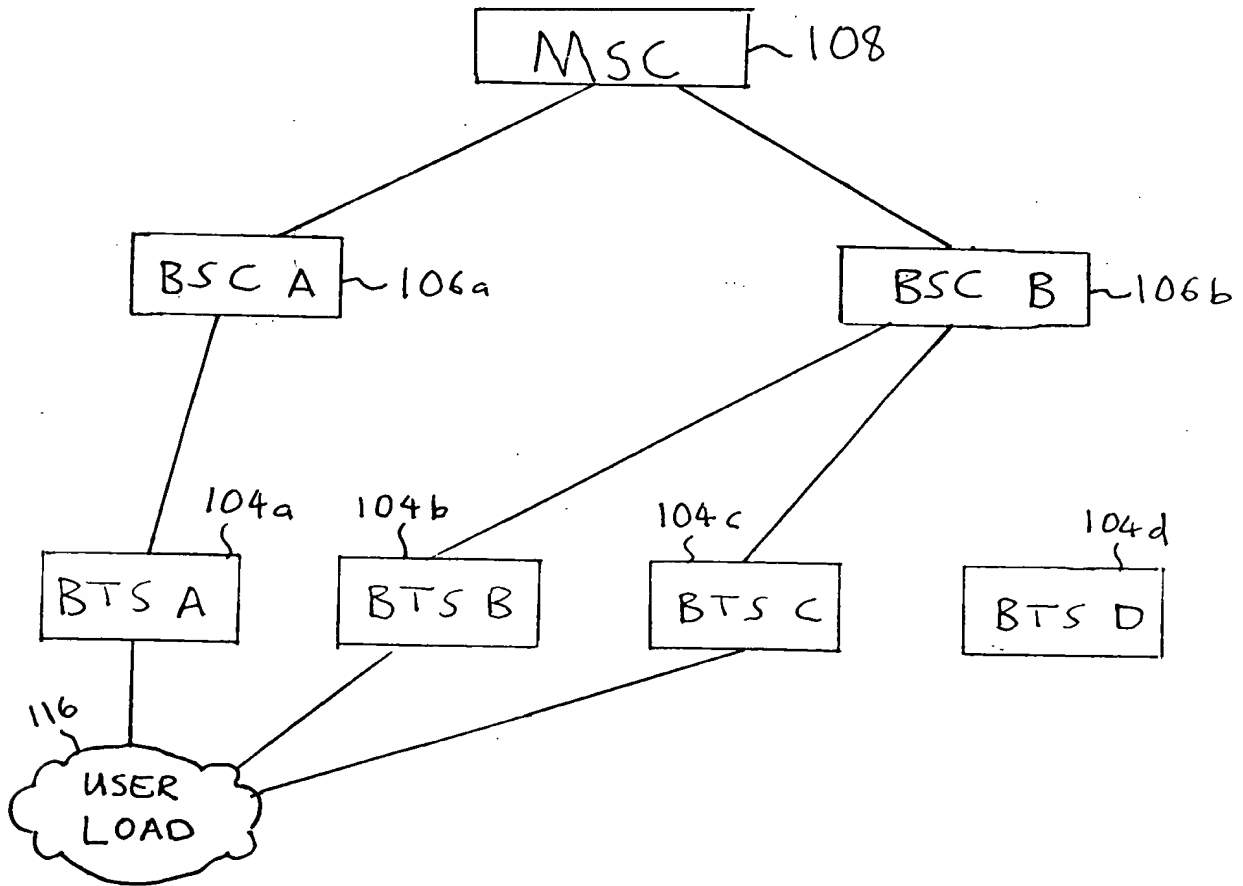


FIG. 5A

FIG. 5B

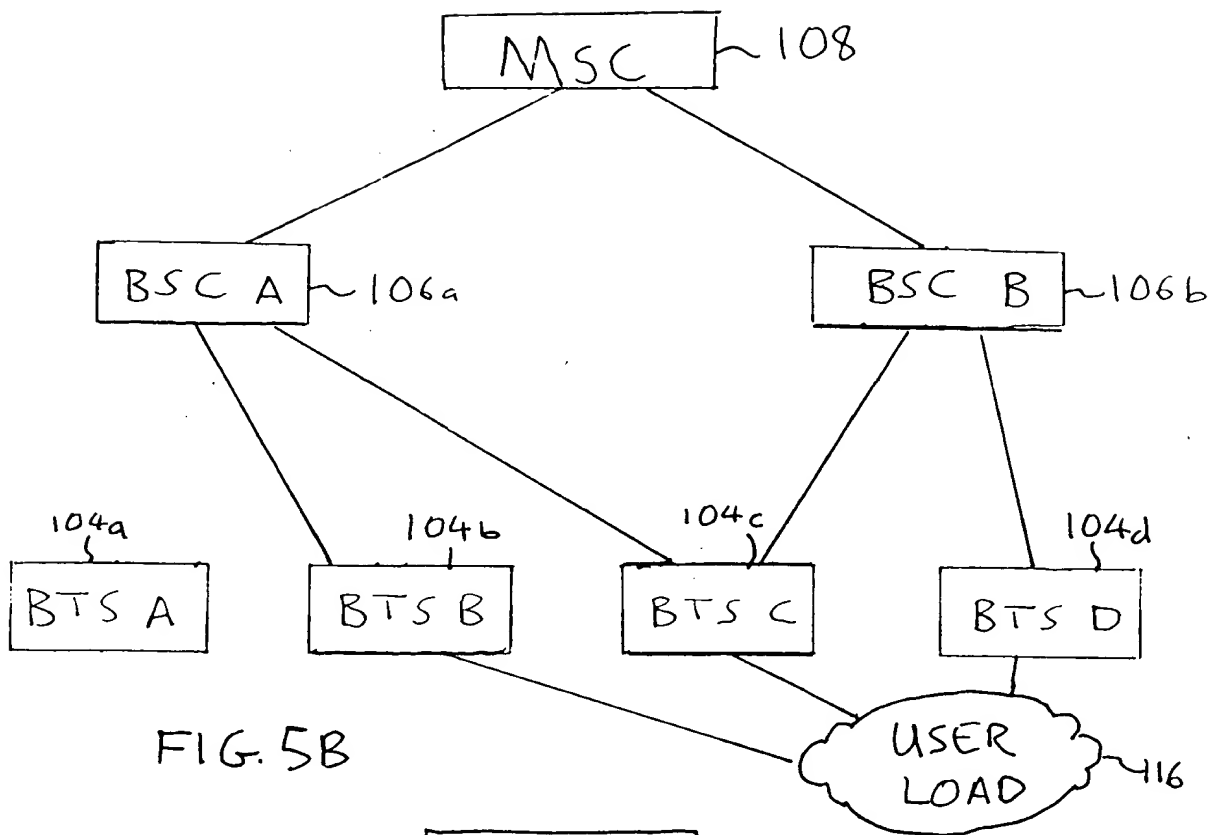


FIG. 5B

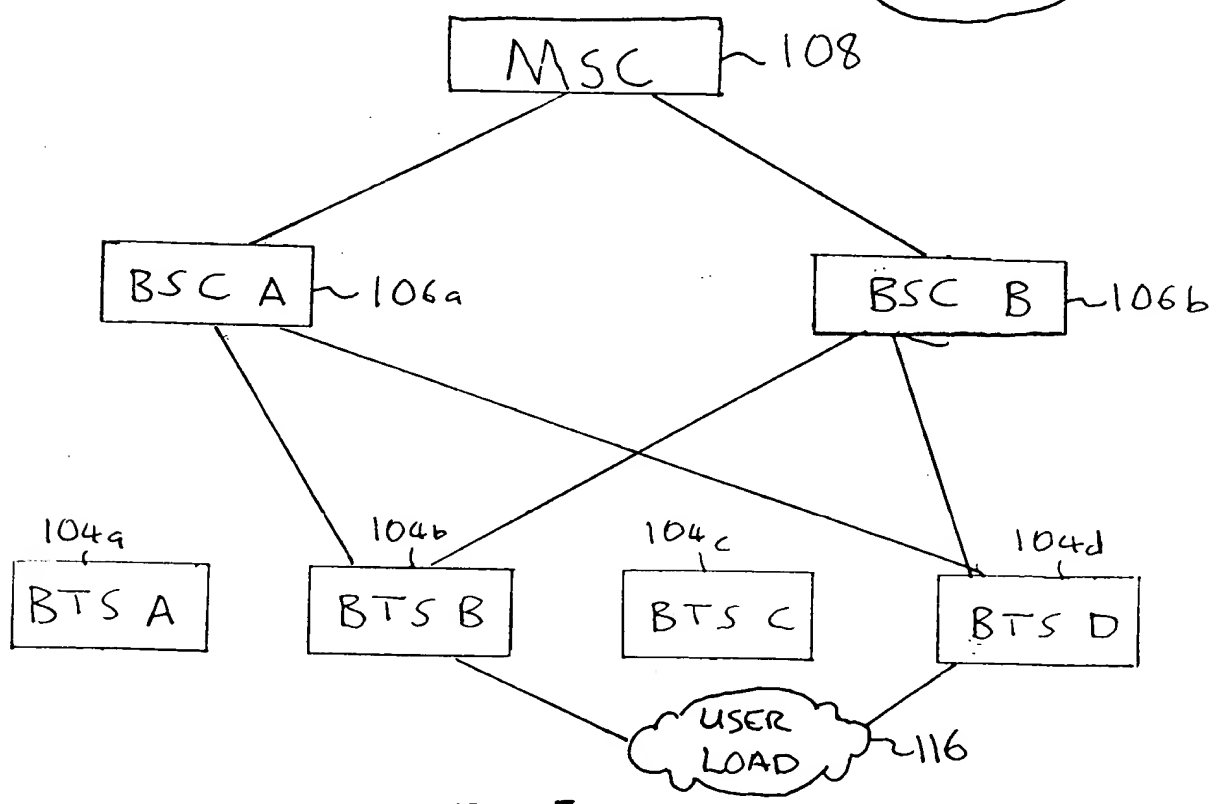


FIG. 5C

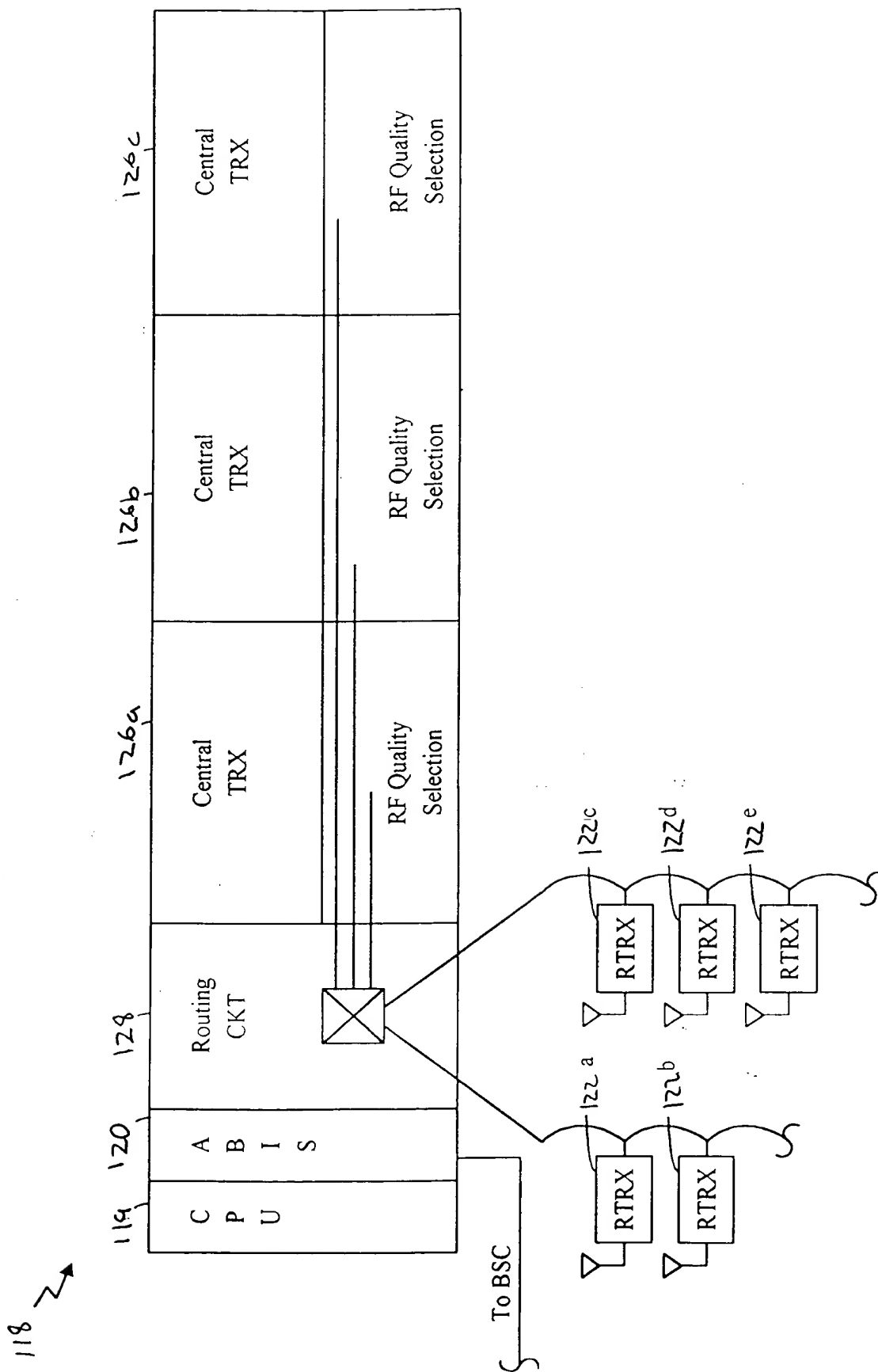


FIG. 7

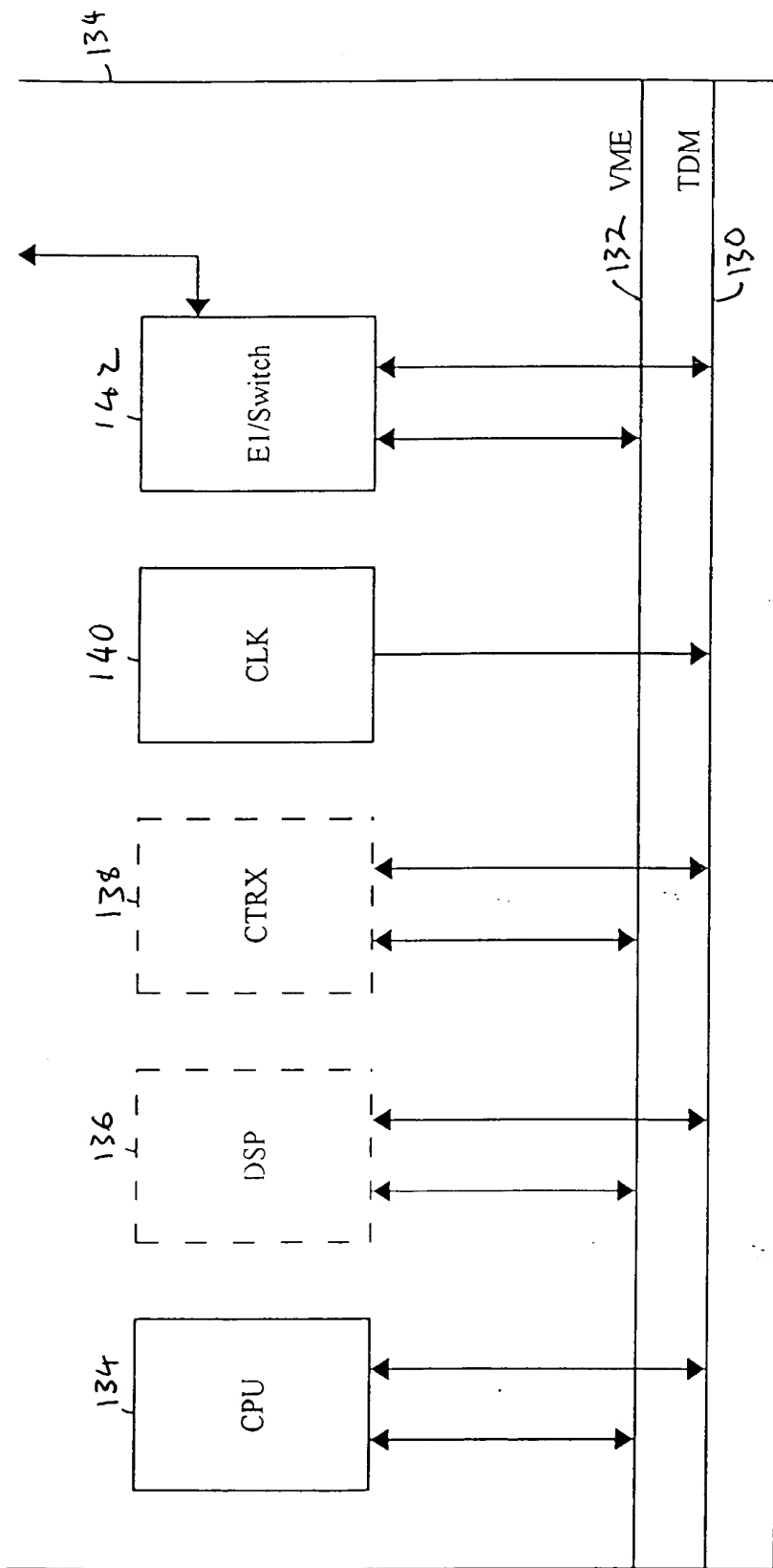


FIG. 8

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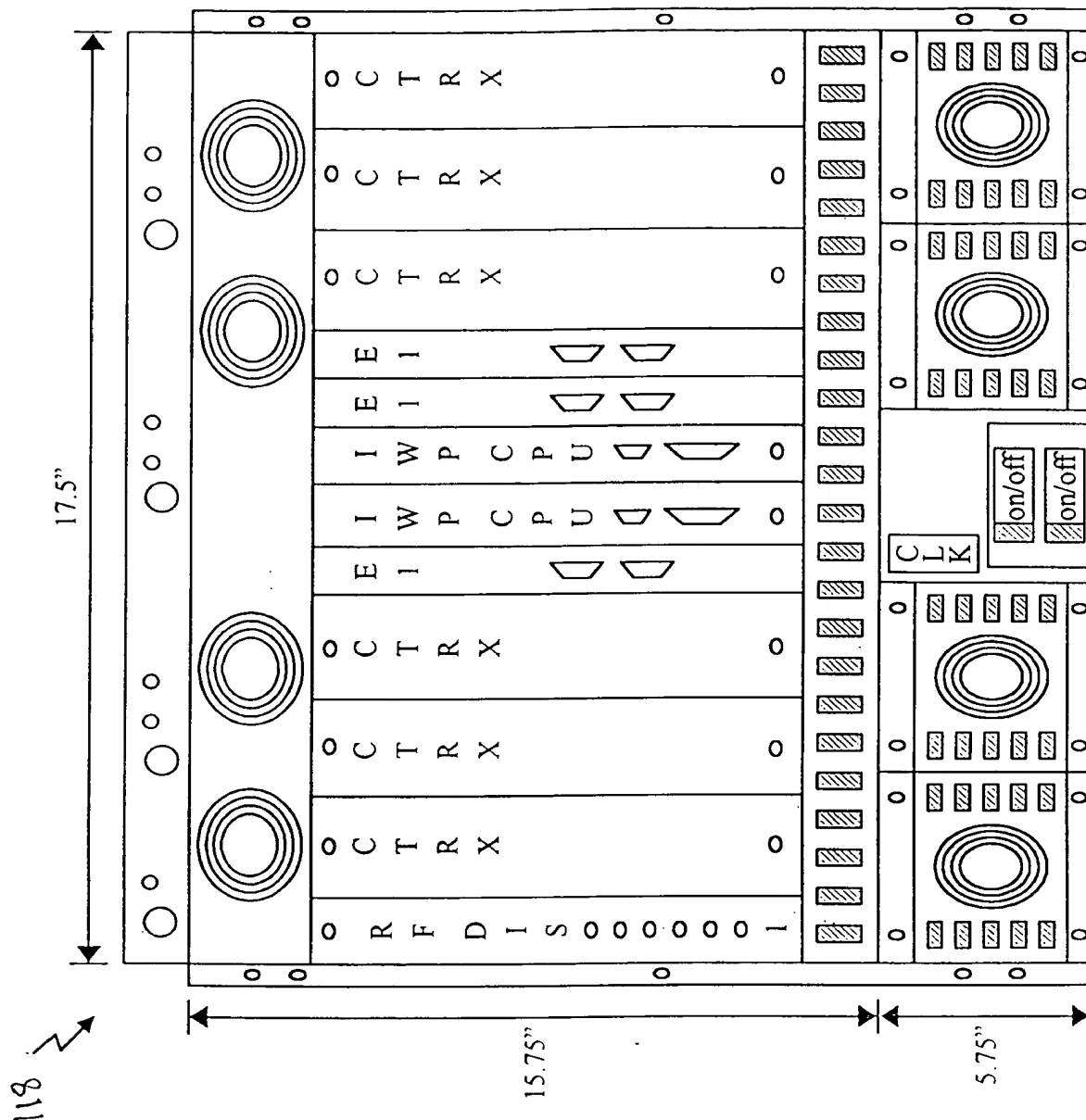


FIG. 9

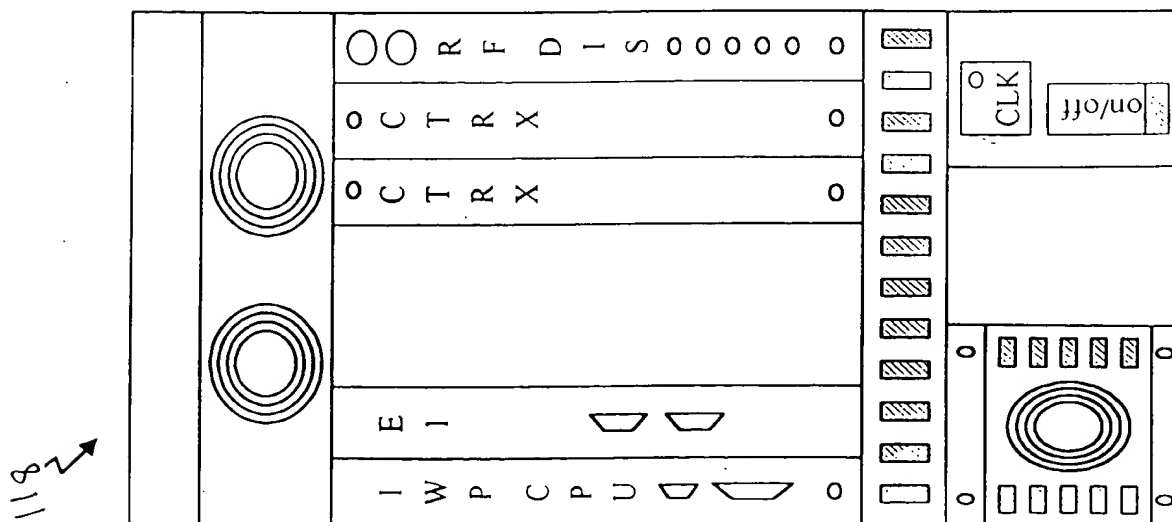


FIG. 10

FIG. 11

CBTS	BSC	CBTS / BSC	MSC	BSC / MSC	CBTS / BSC / MSC
3 CTRX	0 CTRX	2 CTRX	0 CTRX	0 CTRX	2 CTRX
1 TM	6 TM	2 TM	6 TM	6 TM	2 TM
1 CPU	3 CPU	2 CPU	3 CPU	3 CPU	2 CPU
1 Clk	1 Clk	1 Clk	1 Clk	1 Clk	1 Clk
1 RF Dist		1 RF Dist			1 RF Dist

FIG. 11

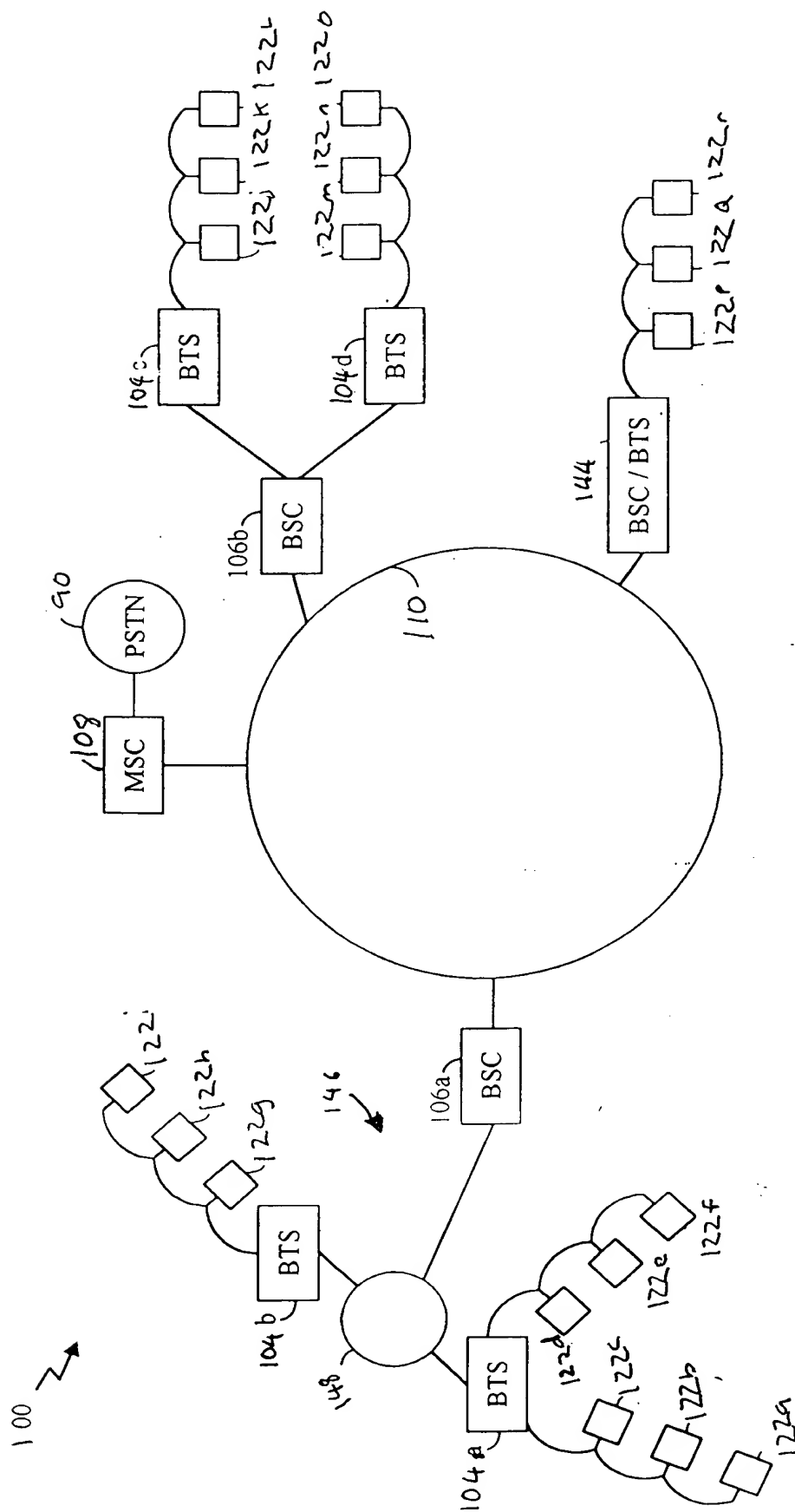


FIG. 15

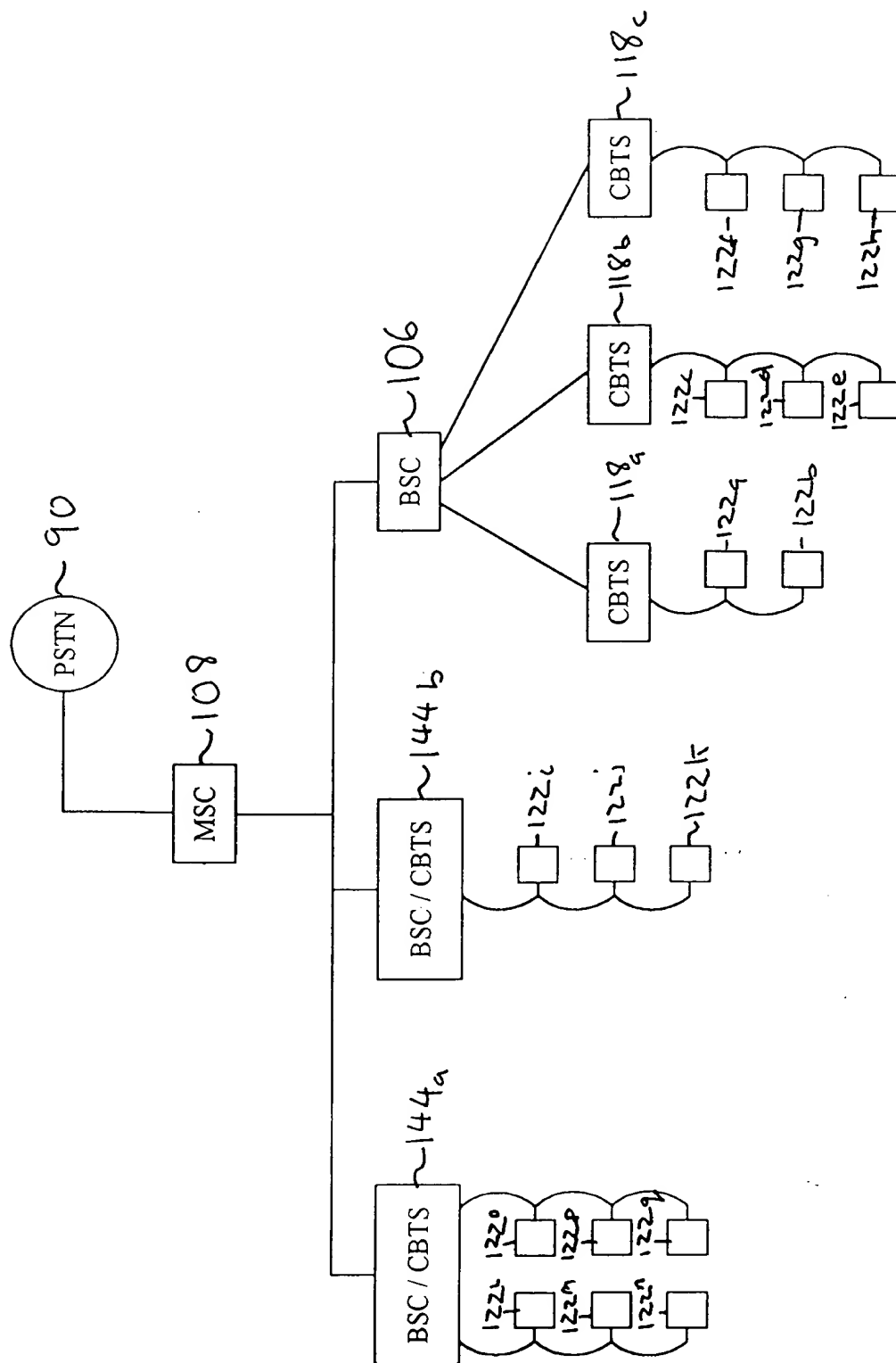


FIG. 12

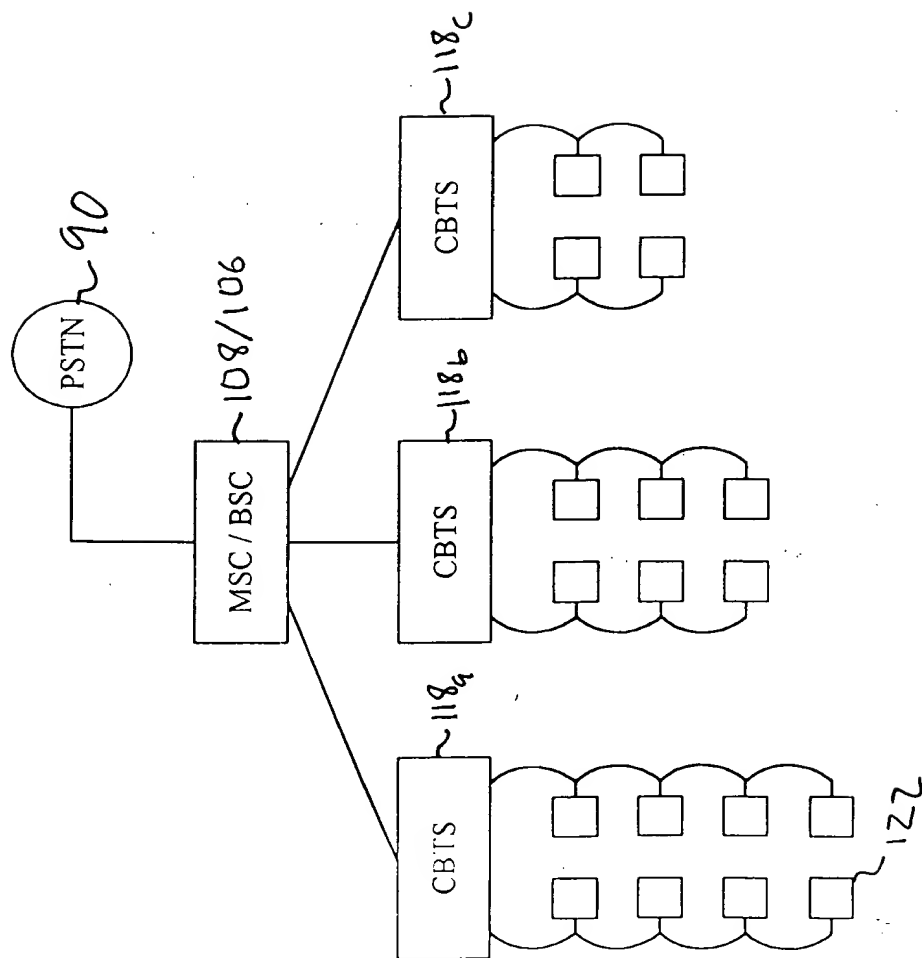


FIG. 13

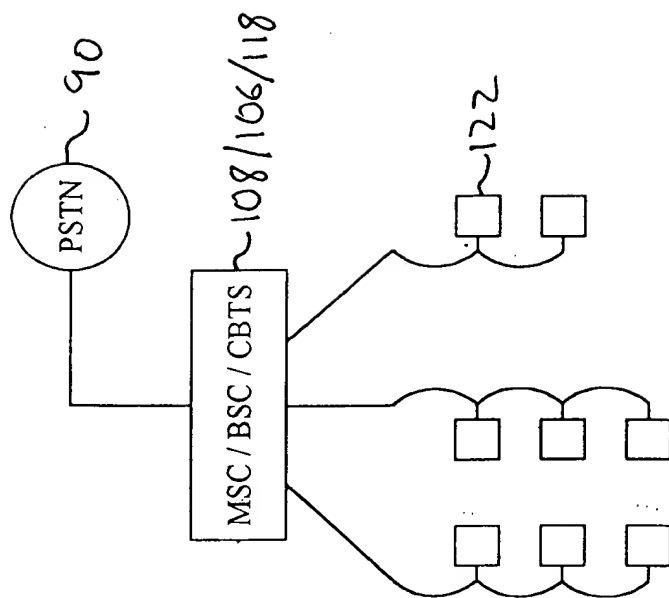


FIG. 14

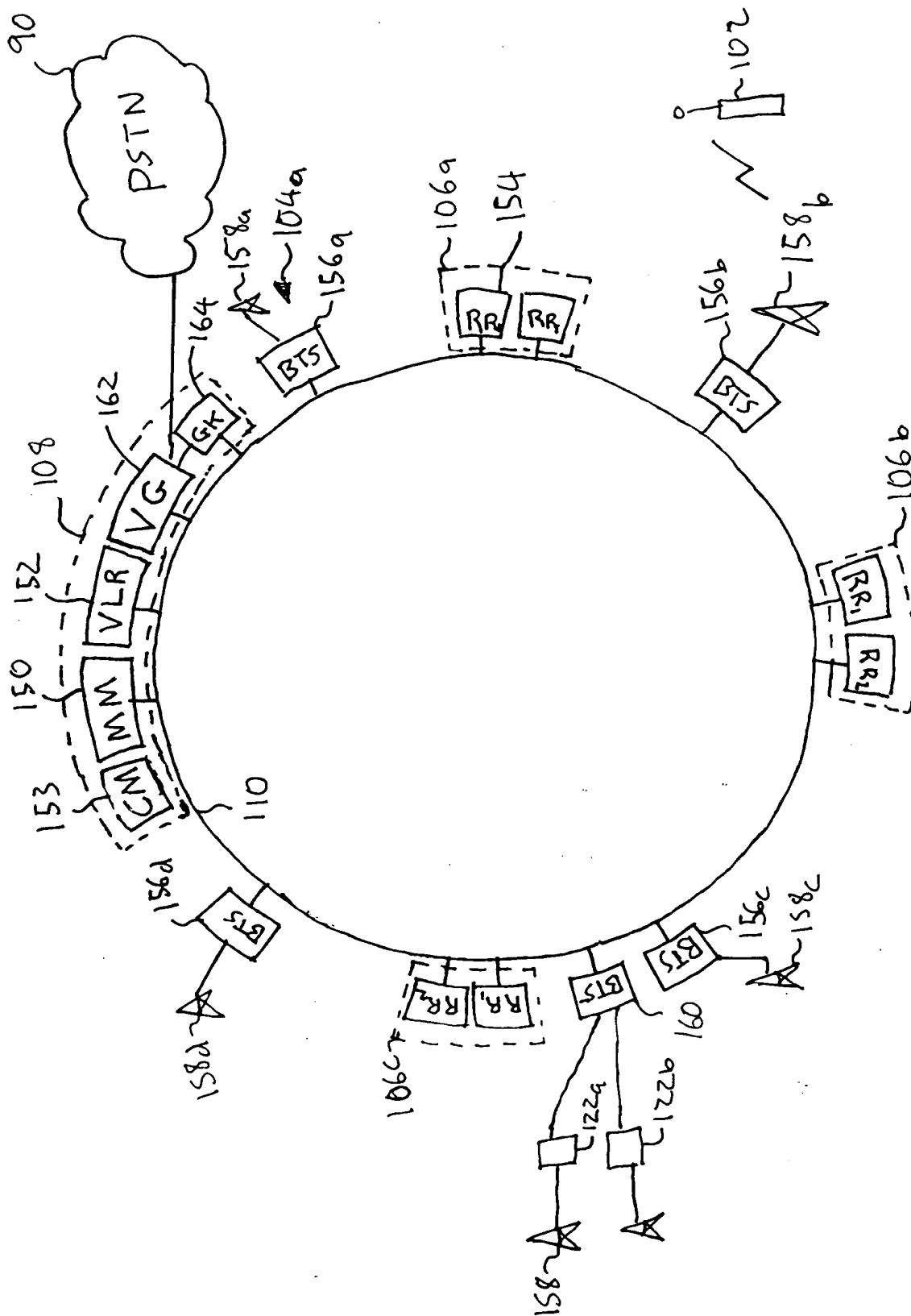


FIG. 16